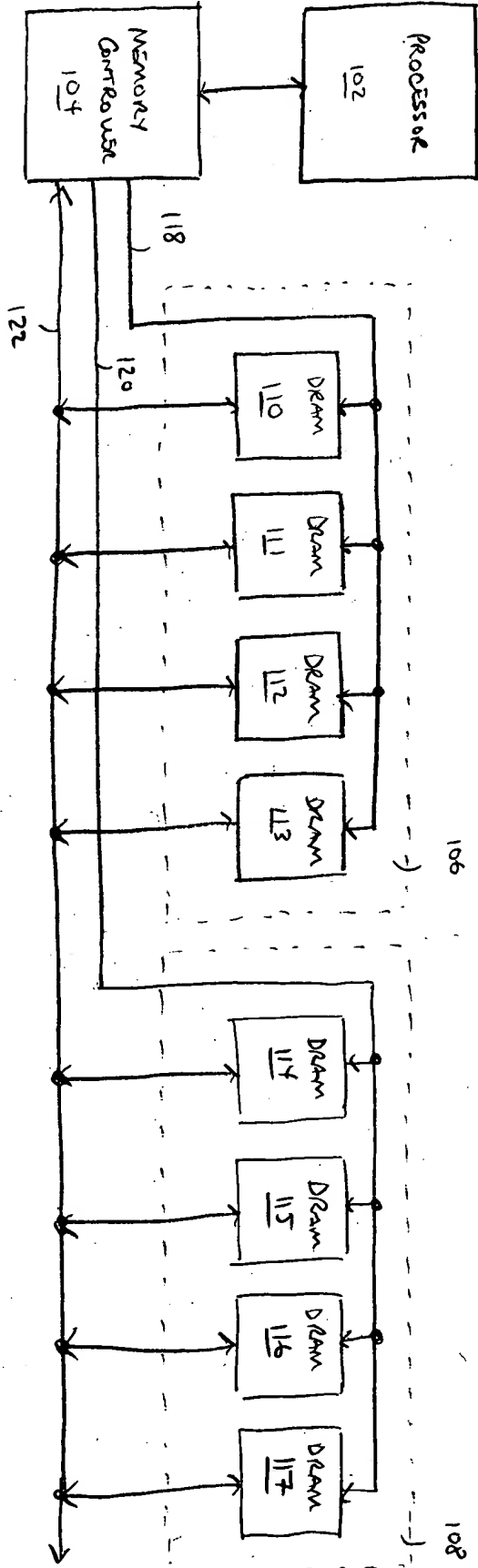




365720" 4E2E2060

FIG. 1 (PRIOR ART)



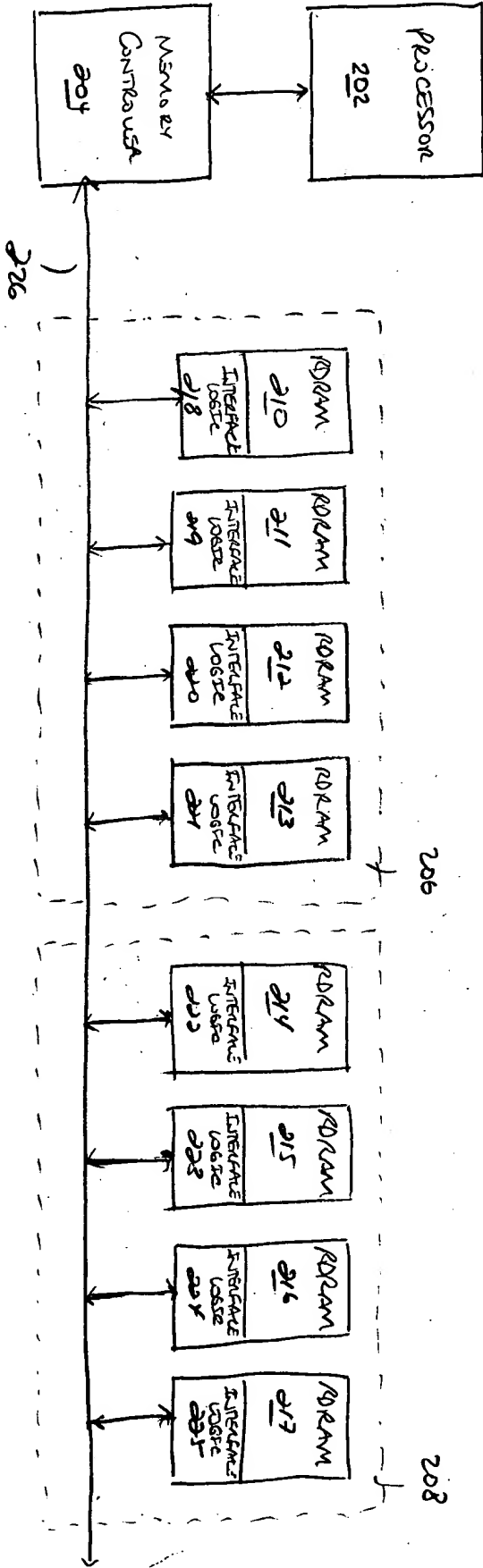


FIG. 2 (Prior Art)









- 500

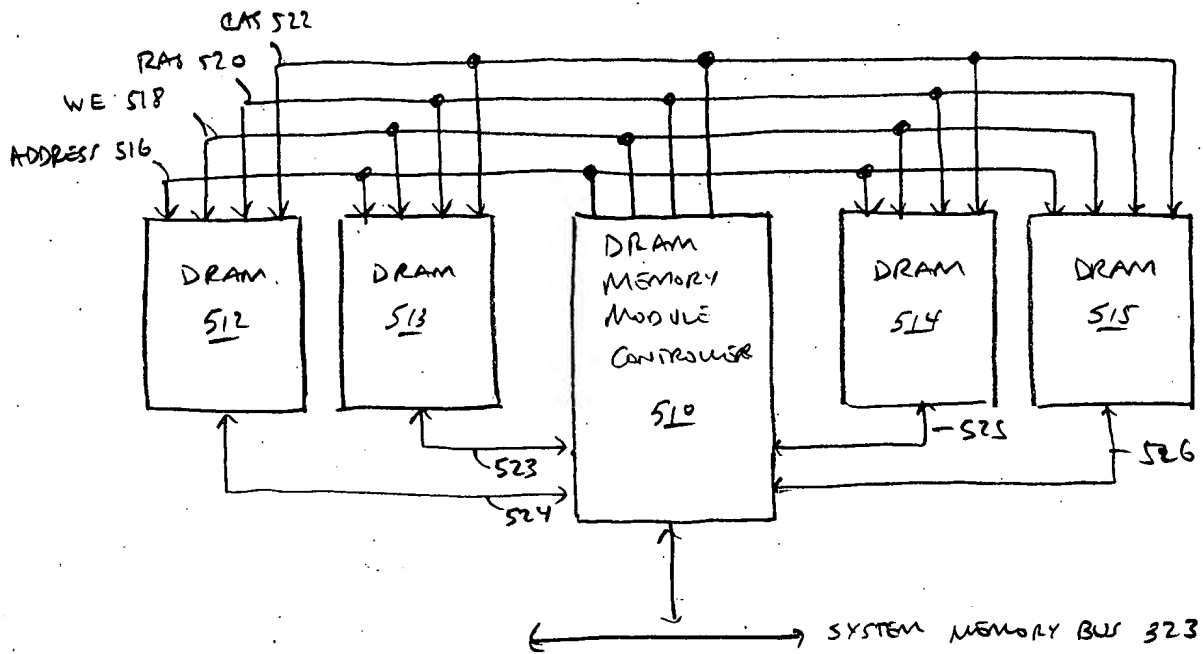


FIG. 5

- 600

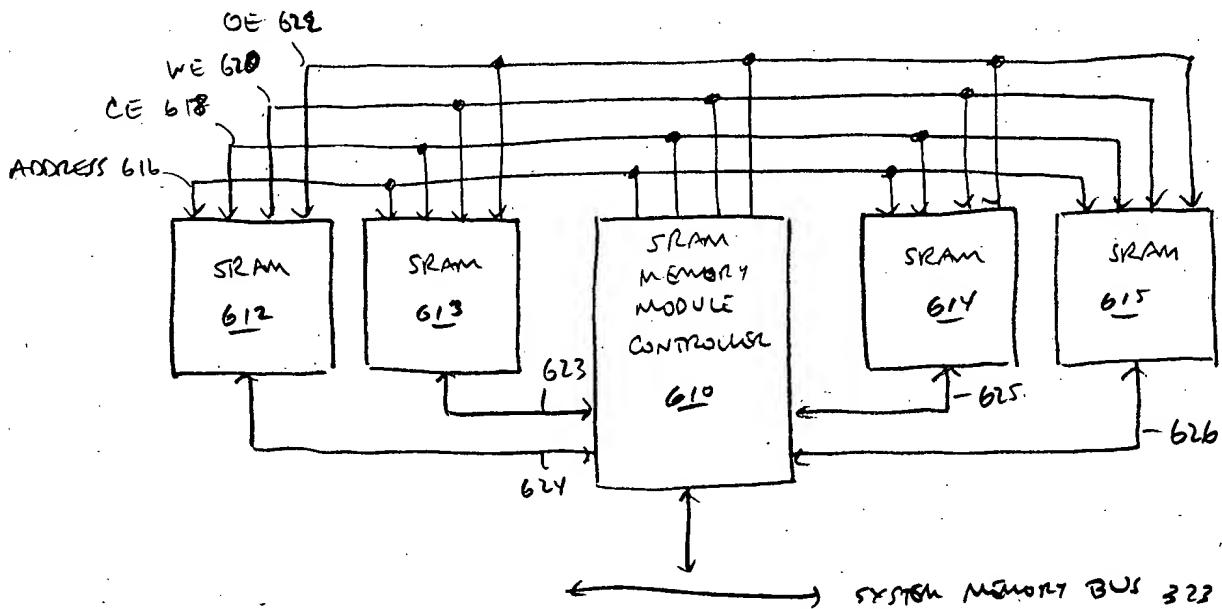


FIG. 6



700

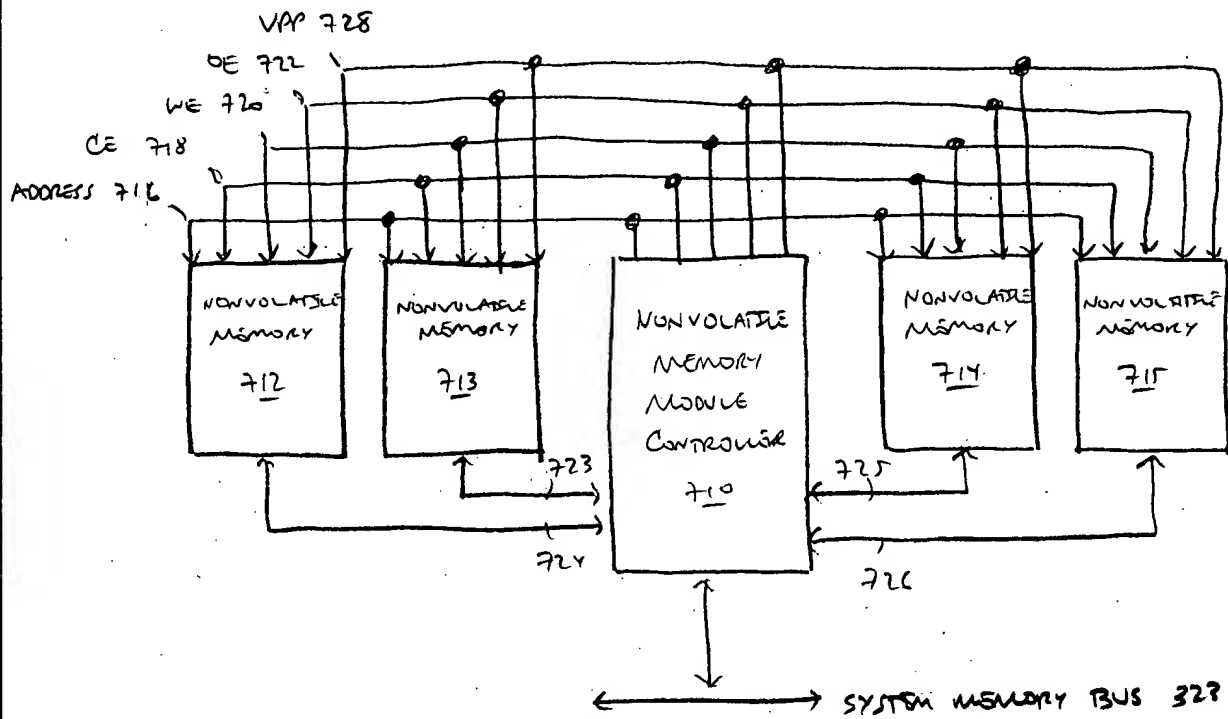


FIG. 7

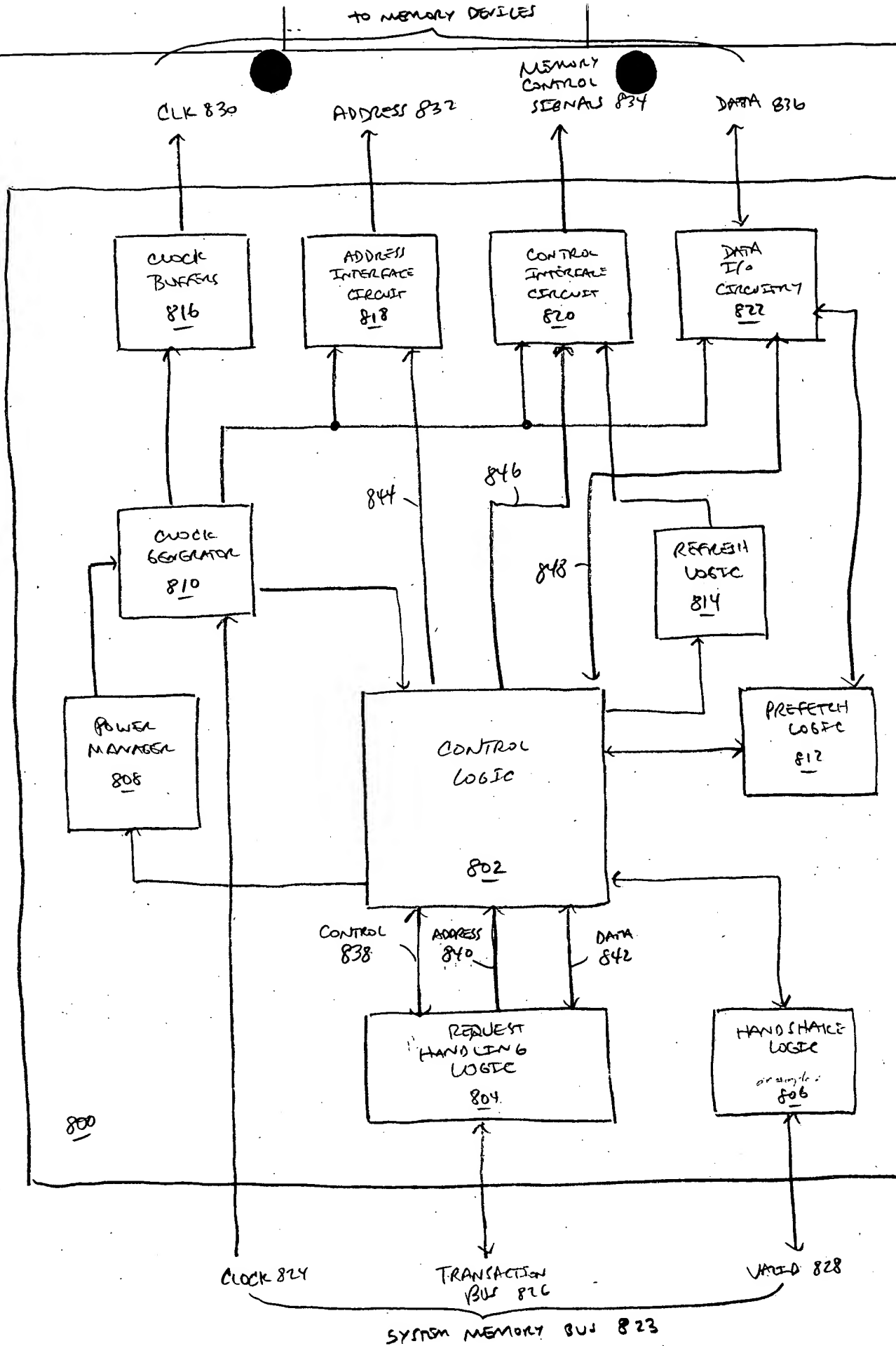


FIG. 8



SECRET 14-00000

900

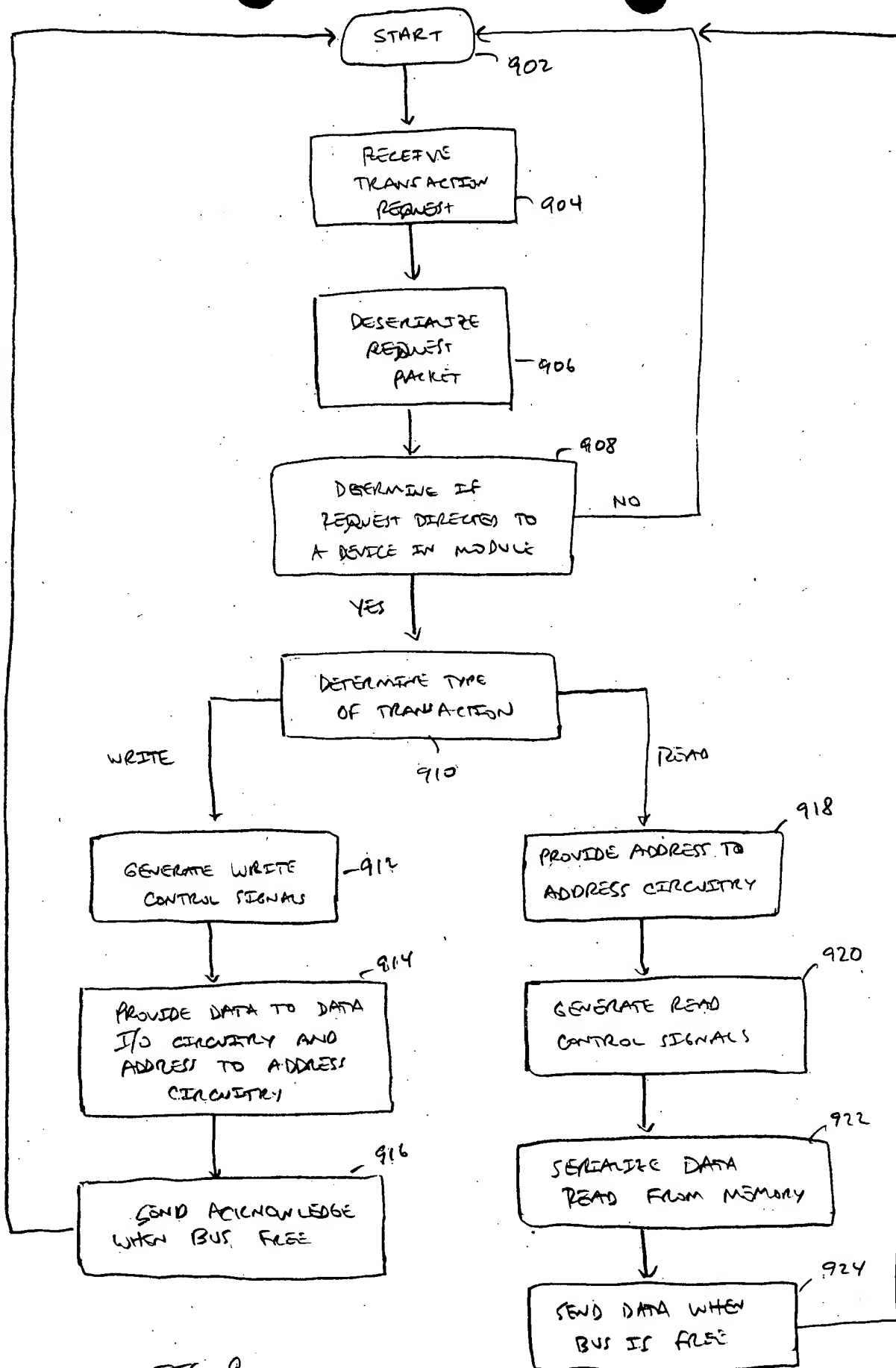
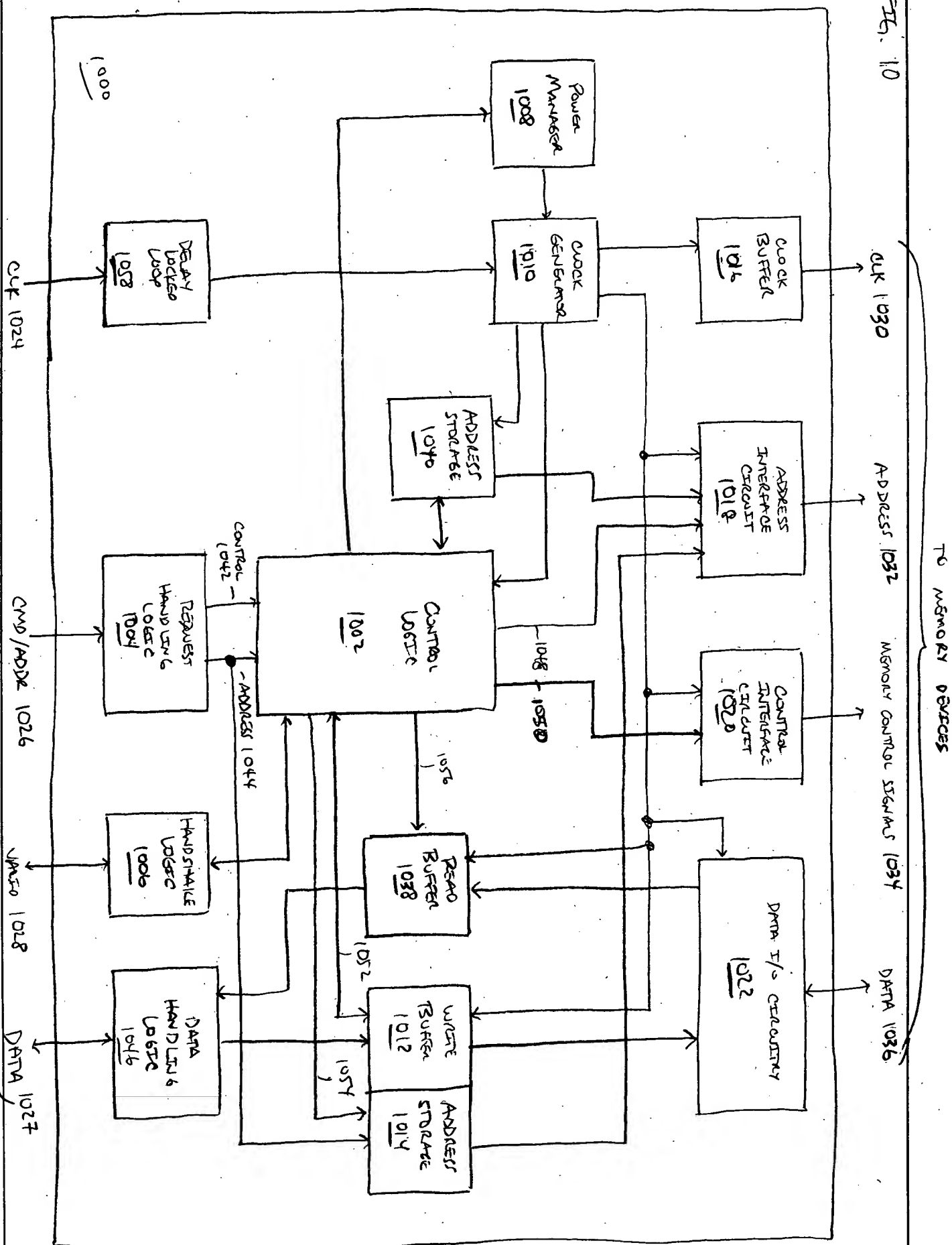


FIG. 9



FIG. 10



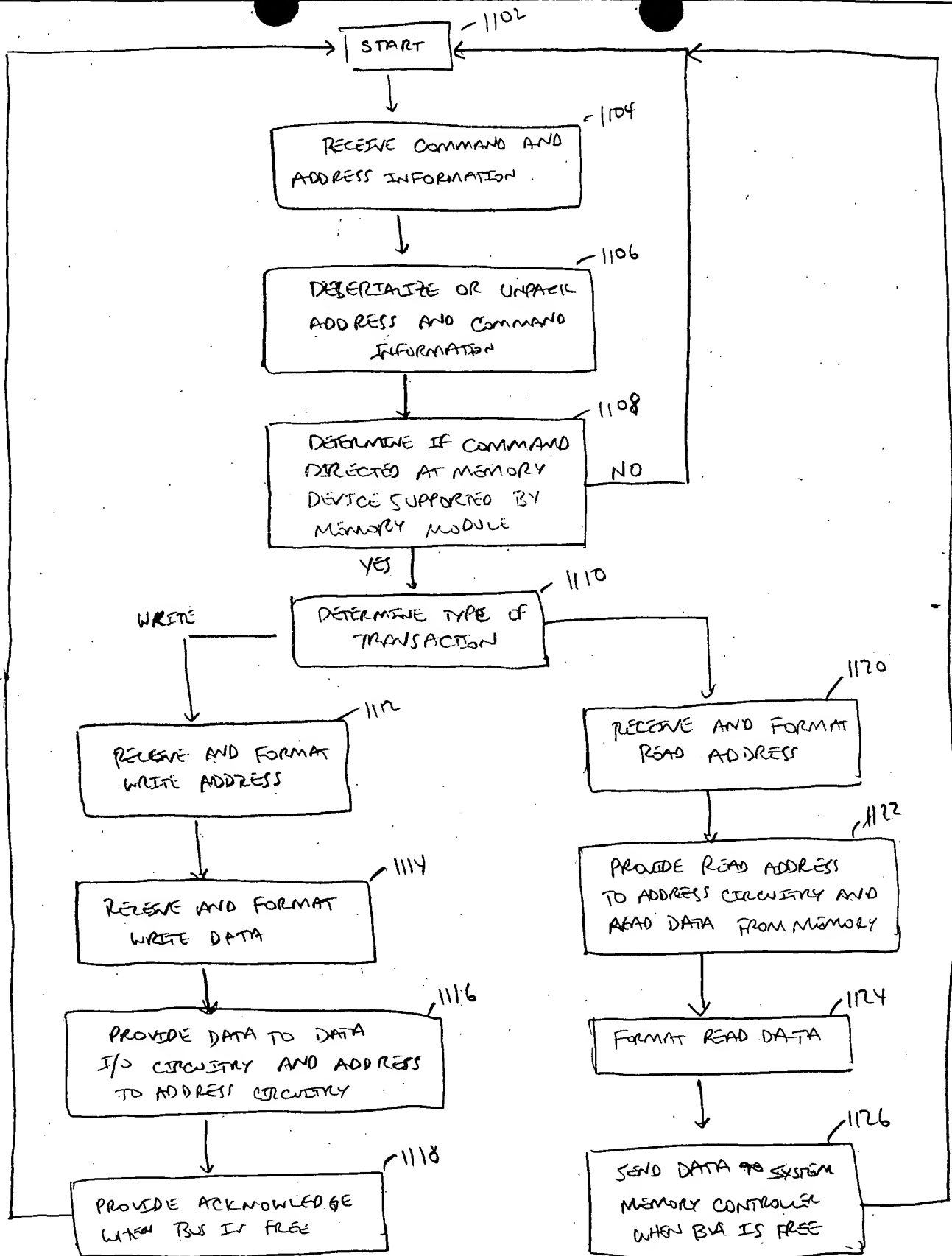
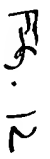
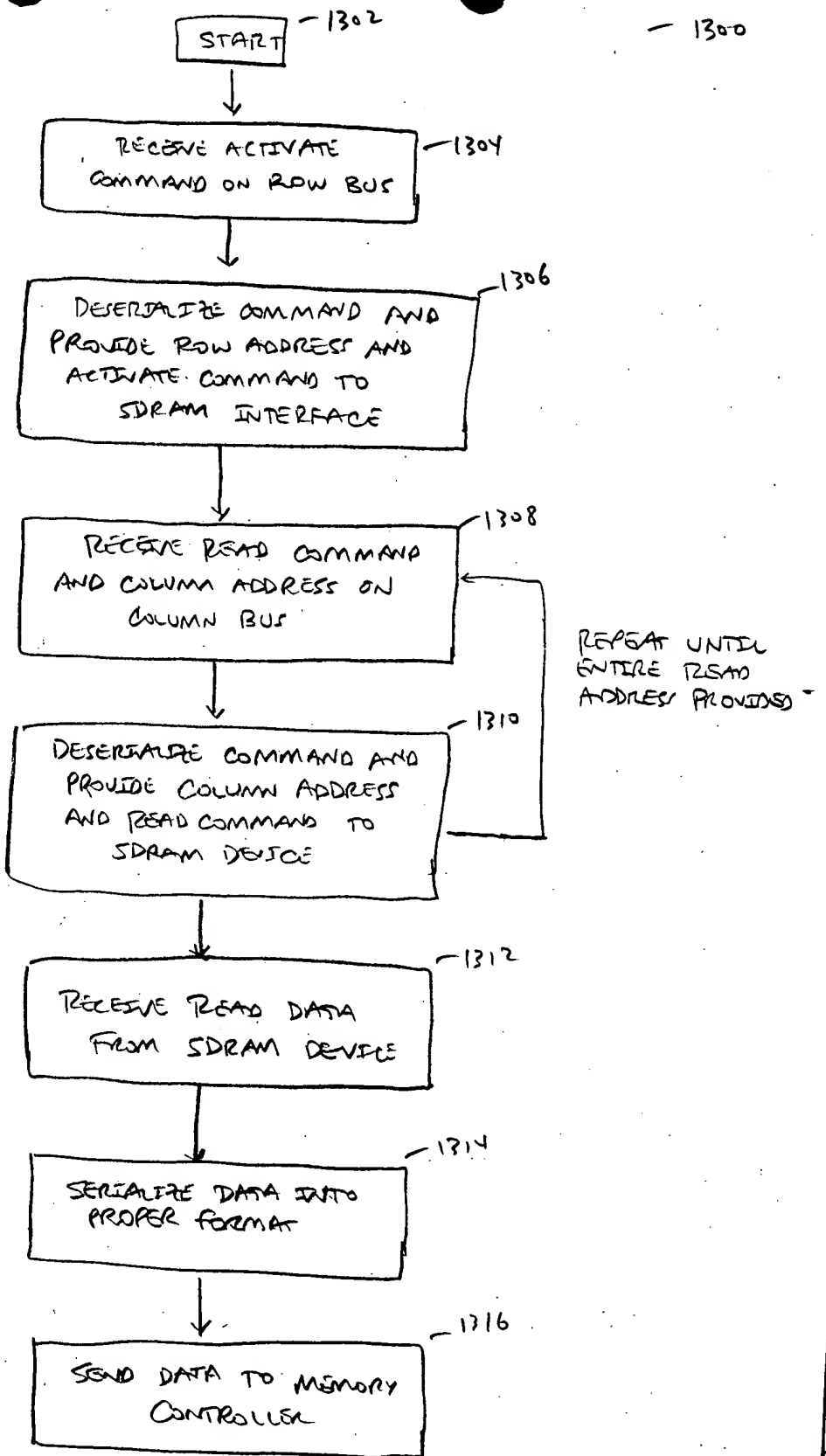


FIG. 11





CLK 1228

Row 1234

Column 1236

DATA 1232

CLK 1208, 1216

ADDR 1214, 1222

Control Bus 1238, 1246

DATA 1224-1227

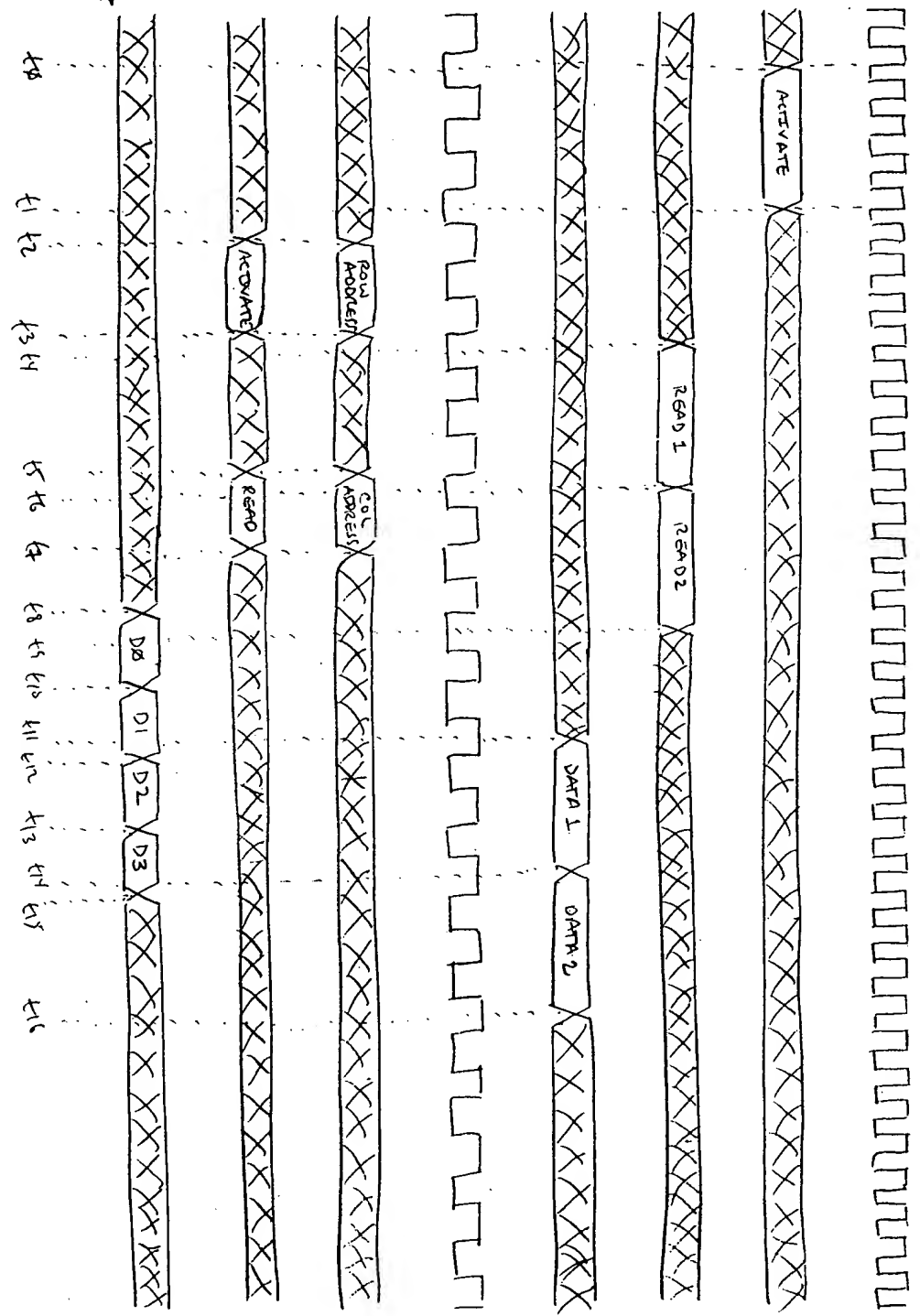


Fig. 14

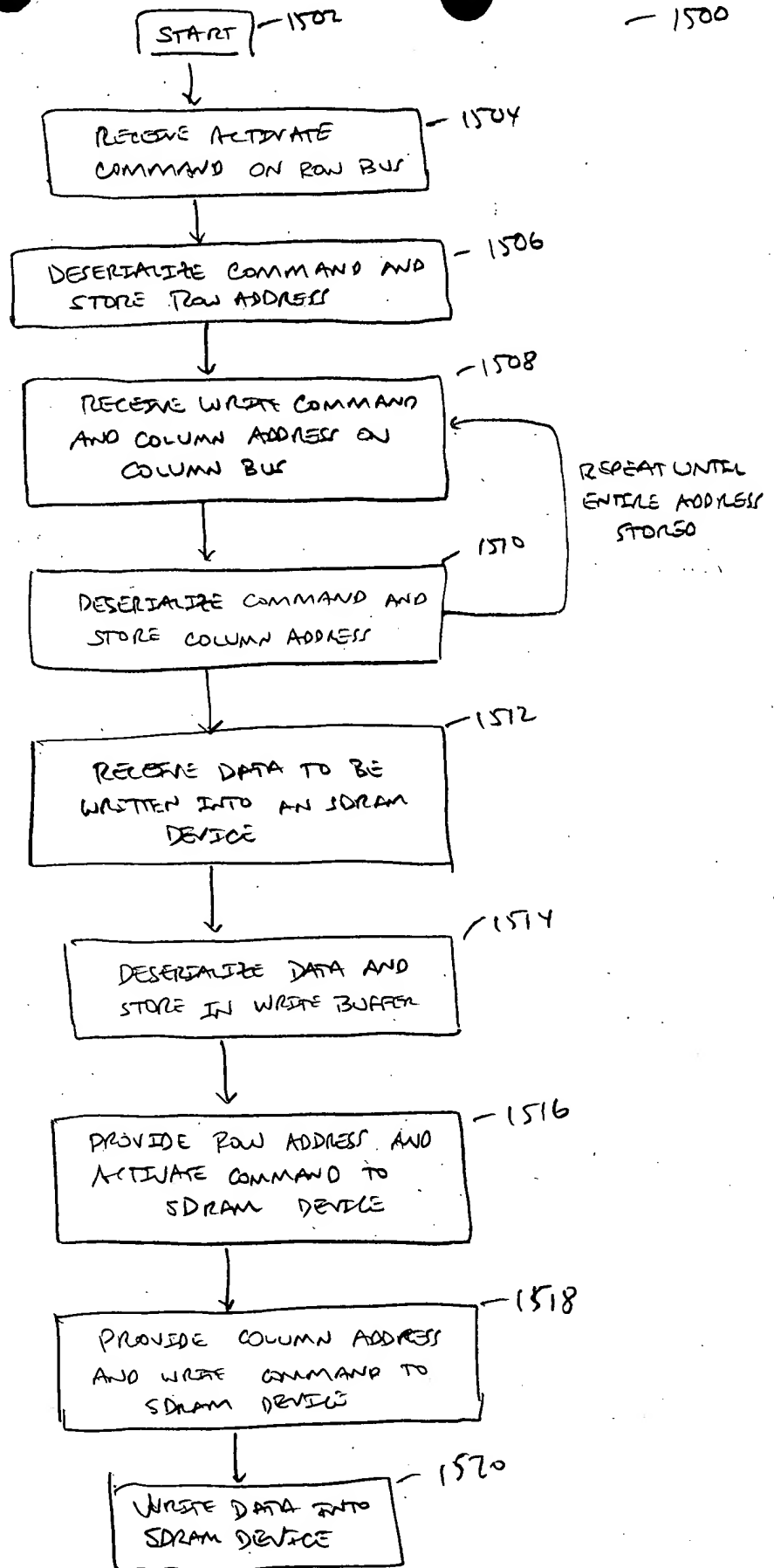
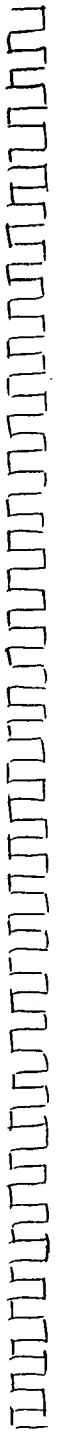


FIG. 15

clk 1228



Row 1234



Column 1236



DATA 1232



clk 1208, 1216



ADDR 1214,  
1222



Control  
BUS 1238, 1240



DATA 1224-  
1227

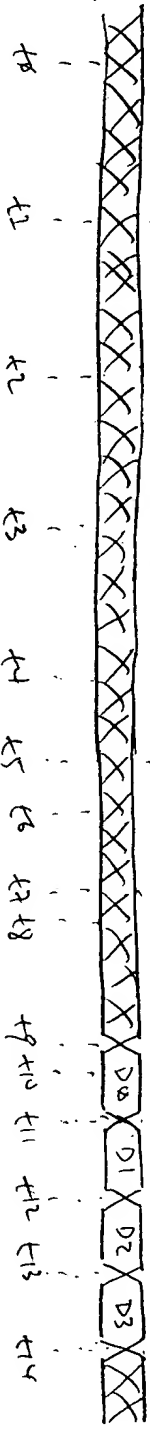
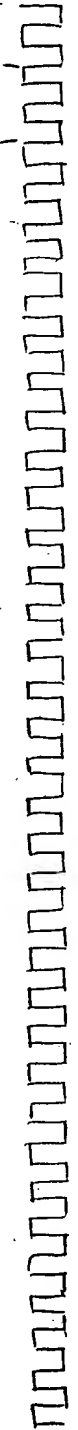


FIG. 16



CLK 1228



CONTROL BUS 1235



DATA 1232



CLK 1208, 1216



ADDR 1214, 1222



CONTROL BUS 1238, 1240



DATA 1214-1217



t0 t1 t2 t3 t4 t5 t6 t7 t8 t9 t10 t11 t12 t13 t14 t15 t16 t17

Fig. 17





CLK 1228



ACTIVE

WRITE 1

WRITE 2

CONTROL BUS 1235



DATA 1232



CLK 1208, 1216



ADDR 1214, 1222



CONTROL BUS 1238, 1240



DATA 1224-1237



t0 t1 t2 t3 t4 t5 t6 t7 t8 t9 t10 t11 t12 t13 t14 t15

FIG. 18

